To the Assistant Commissioner for Patents:

The undersigned assignee of the entire interest in application for letters patent filed herewith, entitled: Semiconductor Package with a Controlled Impedance Bus and Method of Forming Same and having the named inventors: NADER GAMINI and DONALD V. PERINO

hereby appoints the following attorneys to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith; said appointment to be to the exclusion of the inventor(s) and his (their) attorney(s) in accordance with the provisions of 37 C.F.R. 1.32: S. Leslie Misrock (Reg. No. 18872), Harry C. Jones, III (Reg. No. 20280), Berj A. Terzian (Reg. No. 20060), Gerald J. Flintoft (Reg. No. 20823), David Weild, III (Reg. No. 21094), Jonathan A. Marshall (Reg. No. 24614), Barry D. Rein (Reg. No. 22411), Stanton T. Lawrence, III (Reg. No. 25736), Charles E. McKenney (Reg. No. 22795), Philip T. Shannon (Reg. No. 24278), Francis E. Morris (Reg. No. 24615), Charles E. Miller (Reg. No. 24576), Gidon D. Stern (Reg. No. 27469), John J. Lauter, Jr. (Reg. No. 27814), Brian M. Poissant (Reg. No. 28462), Brian D. Coggio (Reg. No. 27624), Rory J. Radding (Reg. No. 28749), Stephen J. Harbulak (Reg. No. 29166), Donald J. Goodell (Reg. No. 19766), James N. Palik (Reg. No. 25510), Thomas E. Friebel (Reg. No. 29258), Laura A. Coruzzi (Reg. No. 30742), Jennifer Gordon (Reg. No. 30753), Jon R. Stark (Reg. No. 30111), Allan A. Fanucci (Reg. No. 30256), Geraldine F. Baldwin (Reg. No. 31232), Victor N. Balancia (Reg. No. 31231), Samuel B. Abrams (Reg. No. 30605), Steven I. Wallach (Reg. No. 35402), Marcia H. Sundeen (Reg. No. 30893), Paul J. Zegger (Reg. No. 33821), Edmond R. Bannon (Reg. No. 32110), Bruce J. Barker (Reg. No. 33291), Adriane M. Antler (Reg. No. 32605), Thomas G. Rowan (Reg. No. 34419), James G. Markey (Reg. No. 31636), Thomas D. Kohler (Reg. No. 32797), Scott D. Stimpson (Reg. No. 33607), Ann L. Gisolfi (Reg. No. 31956), Mark A. Farley (Reg. No. 33170), Gary S. Williams (Reg. No. 31066) and William S. Galliani (Reg. No. 33885), provided that if any one of said attorneys ceases being affiliated with the law firm of Pennie & Edmonds LLP as partner, employee or of counsel, such attorney's appointment as attorney and all powers derived therefrom shall terminate on the date such attorney ceases being so affiliated.

Direct all telephone calls to William S. Galliani at (650) 493-4935.

Address all correspondence to:

PENNIE & EDMONDS LLP 1155 Avenue of the Americas New York, NY 10036-2711

|                |         | Assignee:                        | RAMBUS INCORPORATED   |
|----------------|---------|----------------------------------|---|
|                |         | By:                              | Joel Karp (typed name)                                      |
| Date: <u>ſ</u> | 2/20/55 | Signature:<br>Title:<br>Address: | Vice President  2465 Latham Street  Mountain View, CA 94040 |

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## DECLARATION FOR PATENT APPLICATION

As a below-named inventor, I hereby declare that:

is attached hereto.

[x]

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled Semiconductor Package with a Controlled Impedance Bus and Method of Forming Same the specification of which

| []   | was filed on _<br>and was amer  | as Ap  | plication Serial (if appli   | No<br>cable)  |                                       |
|--|---|--|--|---|---------------------------------------|
| I hereby state specification, include  |   |  |  | ents of the above-<br>t referred to above                     |                                       |
| I acknowledg<br>be material to patent  |   |  |  | information know  | n to me to                            |
| I hereby clain<br>any foreign applicate<br>identified below any<br>before that of the ap   | on(s) for patent<br>foreign applica                                     | or inventor's contion for patent   | ertificate listed<br>or inventor's ce  | ted States Code, § below and have a crtificate having a       | lso                                   |
| Prior Foreign  |   | Priority Claimed   |  |   |                                       |
| (Number)   | (Cour   | ntry)  | (Date Filed)   | _ Yes []  | No [ ]                                |
| I hereby clai<br>States application(s)<br>this application is no<br>by the first paragrap<br>disclose to the Pater<br>defined in 37 C.F.R<br>the national or PCT | listed below are of disclosed in the of Title 35, Up of Office all info | nd, insofar as the prior United nited States Coormation known curred between | e subject matte States applicat de, §112, I ack to me to be ma the filing date | ion in the manner<br>nowledge the duty<br>aterial to patentab | aims of<br>provided<br>to<br>ility as |
| (Application   | Serial No.)   | (Filing Date)  |  | (Status) nted, pending, aba                                   | ndoned)                               |
| I hereby clai<br>States provisional a  |   |  | Inited States Co   | ode §119(e) of any  | / United                              |
| (Application   | n Serial No.)   | (Filir   | g Date)  |   |                                       |

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File No. <u>9797-027-999</u>

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18, United States Code, §1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.